



Atty. Docket No.: NLMI.P146

PATENT

IN THE UNITED STATES PATENT OFFICE

In Re Patent Application of:

First Named Inventor: Nataraj, Bindiganavale S.

Application No.: 10/734,464

Filed: 12/12/2003

Title: CONTENT ADDRESSABLE MEMORY WITH
MODE-SELECTABLE MATCH-DETECT TIMING

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Examiner: Not Yet Assigned

Art Unit: Not Yet Assigned

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450 on February 13, 2004

Charles E. Shemwell

(Name of Person Mailing Correspondence)

 2-13-2004

Signature

Date

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

Sir:

Enclosed is an Information Disclosure Citation Form PTO/SB/08A together with a copy of each reference cited therein. It is respectfully requested that the cited references be considered and that the enclosed copy of the Form PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant.

This Information Disclosure Statement is being submitted pursuant to 37 CFR 1.97(b). No fee is believed to be due.

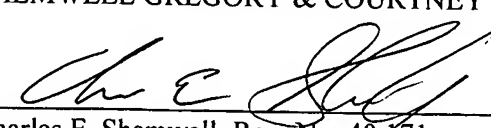
Pursuant to 37 CFR 1.97(h), the submission of this Information Disclosure Statement is not to be construed as an admission that the information cited in this statement is material to patentability.

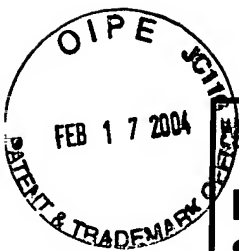
The Commissioner is hereby authorized to charge any fees which may be required in connection with this submission to Deposit Account No. 501914.

Respectfully submitted,

SHEMWELL GREGORY & COURTNEY LLP

Date February 13, 2004


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Substitute for Form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Application No.	10/734,464
Filed	12/12/2003
First Inventor	Nataraj, B.
Art Unit	
Examiner	
Atty. Docket No.	NLMI.P146

U.S. Patent Documents					
Examiner Initials*	US Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication	Relevant Pages, Columns, Lines
	Number	Kind			
	2002/0093347	A1	Henderson et al.	7/18/2002	
	6,195,277		Sywyk et al.	2/27/2001	
	6,199,140		Srinivasan et al.	3/6/2001	
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	6,351,143		Guccione et al.	2/26/2002	
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Non Patent Literature Documents		
Examiner Initials	Name of Author, Title of Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country where Published	Trans- lation
	"Self-Timed Hit Circuit for a Content Addressable Memory," IBM Technical Disclosure Bulletin, Vol. 38, No. 2, Feb. 1995, pp. 65-66 plus cover and end page	

Examiner Signature		Date Considered	
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.